IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jae-han CHA) I hereby certify that this Patent
	Application Transmittal and the
Serial No.: To be assigned	documents referred to as enclosed
3	therewith are being deposited with the
Filed: July 29, 2003 (herewith)	United States Postal Service on
riled. July 29, 2003 (nelewith)) July 30, 2003, in an envelope addressed
) to the Commissioner for Patents, P.O.
For: METHOD FOR FABRICATING	Box 1450, Alexandria, Virginia 22313-
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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Submitted herewith for consideration by the examiner is a copy of the document identified on the attached PTO SB/08. Entry and consideration of the submitted document is solicited.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 13-2855.

Respectfully submitted,

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July 30, 2003

By:

Michael R. Hull

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Substitute for form 1449A/PTO		Complete if Known			
				Application Number	To be assigned
11	NFORMATIO	ON DIS	CLOSURE	Filing Date	July 29, 2003 (herewith)
S	STATEMENT BY APPLICANT		First Named Inventor	Jae-han CHA	
			Art Unit	To be assigned	
(use as many sheets as necessary)	Examiner Name	To be assigned			
Sheet	1	of	1	Attorney Docket Number	29925/39488

	U.S. PATENT DOCUMENTS				
Examiner	Cite	Document Number	Publication Date	Name of Patentee or Applicant	Pages, Columns, Lines, Where Relevant
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FOREIGN PATENT DOCUMENTS					
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¹ Applicant's unique citation designation number (optional). ² See attached Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁸ Applicant is to place a check mark here if English language Translation is attached.

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		Kinoshita et al., "Process Integration Technology for sub-30 ps ECL BiCMOS using Heavily Boron Doped Epitaxial Contact (HYDEC)," <i>Electron Devices Meeting</i> , 1994. <i>Technical Digest</i> , <i>International</i> , 11-14 Dec 1994 pp. 441-444.			
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